

Building Electronic Devices from Clusters

S. A. Brown^{1,2,3}, J. G. Partridge^{1,3}, R. Reichel^{1,3}, A. Ayesh^{1,3}, P. Convers^{1,3}, A. Sattar^{1,3}, J. van Lith², A. Lassesson², F. Natali^{1,3}, E. Boyd^{1,3}, K. C. Tee^{1,2}, D. M. A. Mackenzie^{1,3}, A. Awasthi^{1,4}, S. C. Hendy^{1,4,5}, P. Zoontjens^{1,5}.

¹ *MacDiarmid Institute of Advanced Materials and Nanotechnology*

² *Nano Cluster Devices Ltd, Christchurch, New Zealand,*

³ *Department of Physics and Astronomy, University of Canterbury, Christchurch, New Zealand,*

⁴ *Industrial Research Ltd, PO Box 31-310, Lower Hutt, New Zealand,*

⁵ *Victoria University of Wellington, PO Box 600, Wellington, New Zealand*

simon.brown@canterbury.ac.nz

Regular attendees at ISSPIC conferences are well aware of the enormous variety of novel physical and chemical phenomena observed in clusters. Over the years, fundamental studies of gas phase properties have evolved towards more applied studies of clusters on surfaces, and the possibility of “new” materials formed from clusters. A logical continuation of this evolution is the use of clusters as building blocks for electronic devices.

We believe that the ready availability of clusters with sizes ranging from a few atoms up to tens of nanometres makes them ideal building blocks for electronic devices. This size range spans the gap between current integrated circuits (~50nm) and the molecular scale (<1nm), which is presumably the ultimate future of electronics.

We have developed self-assembly / directed-assembly strategies [1, 2, 3, 4] which provide a potentially manufacturable route to fabrication of cluster-based electronic devices. We have focussed on the formation of contacted cluster chains / nanowires because devices containing such nanowires have a wide range of applications (from chemical sensors to transistors). In this paper we will review our work on nanowire formation using templates [1, 2], stencils [3] and percolation methods [4], as well as our understanding of the basic physical assembly processes (enhanced by extensive Molecular Dynamics simulations [5]). We will also discuss prototype hydrogen sensors based on Pd clusters [6], H₂ and NH₃ gas sensors based on oxidised Sn clusters [7], the use of Cu clusters for interconnect applications [8], and progress toward the fabrication of cluster-assembled transistors [9].

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